# **OSRAM** LZ4-01R608 **Datasheet**



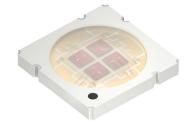


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## LED ENGIN LuxiGen™

# LZ4-01R608

Industry's most robust high power ceramic package with silicone flat top for high performance over life.





## **Applications**

- Access Control & Security
- Appliances & Tools
- Factory Automation
- Home & Building Automation

- Material Processing
- Medical Lighting
- Robotics

#### **Features**

- Package: Ceramic package, clear silicone

- Chip technology: IR Thinfilm

- Typ. Radiation: 120°

- Color:  $\lambda_{peak}$  = 850 nm (• infrared (850 nm))

- Double stack emitter



# **Ordering Information**

Туре Ordering Code Total radiant flux 1)

 $I_{\rm F} = 700 \, \text{mA}$ 

1.6 ... 3.0 W Q65113A1754 LZ4-01R608-0000



Maximum Ratings			
Parameter	Symbol		Values
Operating Temperature	T <sub>op</sub>	min.	-40 °C
	op.	max.	125 °C
Storage Temperature	T <sub>stg</sub>	min.	-40 °C
	3.9	max.	125 °C
Junction Temperature 2)	T <sub>j</sub>	max.	145 °C
Forward Current 2)	I <sub>F</sub>	max.	1000 mA
Forward Current pulsed	I <sub>F pulse</sub>	max.	5000 mA
t ≤ 150 µs; D ≤ 0.1 ; T <sub>C</sub> = 25 °C			
ESD withstand voltage	$V_{\scriptscriptstyle{ESD}}$	ESD sensitive device	
acc. ANSI/ESDA/JEDEC JS-001 (HBM, Class 0)	205		
Reverse voltage 3)	$V_R$		Not designed for
-	K		reverse operation



## **Characteristics**

 $\rm I_{\scriptscriptstyle F}$  = 700 mA;  $\rm T_{\scriptscriptstyle C}$  = 25 °C; all chips operated in series

Parameter	Symbol		Values	
Peak Wavelength 4)	$\lambda_{_{ m peak}}$ min.		835 nm	
$I_{\rm F} = 700  \text{mA}$	poun	typ.	850 nm	
		max.	875 nm	
Viewing angle at 50% $\rm I_{\rm V}$	2φ	typ.	110 °	
Forward Voltage 5)6)	$V_{F}$	min.	10.80 V	
$I_{\rm F} = 700  \text{mA}$	·	typ.	11.85 V	
		max.	14.80 V	
Reverse current 3)	l <sub>R</sub>		Not designed	
			for reverse	
			operation	
Electrical thermal resistance junction/case	R <sub>thJC elec.</sub>	typ.	2.8 K / W	



## **Brightness Groups**

all chips operated in series

Group	Total radiant flux 1)	Total radiant flux 1)	
	$I_{F} = 700 \text{ mA}$	$I_{F} = 700 \text{ mA}$	
	min.	max.	
	$\Phi_{E}$	$\Phi_{E}$	
PQ	1.6 W	2.4 W	
R	2.4 W	3.0 W	

## **Wavelength Groups**

all chips operated in series

Group	Peak Wavelength 4)	Peak Wavelength 4)	
	I <sub>F</sub> = 700 mA	I <sub>F</sub> = 700 mA	
	min.	max.	
	$\lambda_{\sf peak}$	$\lambda_{\sf peak}$	
F08	835 nm	875 nm	

## **Group Name on Label**

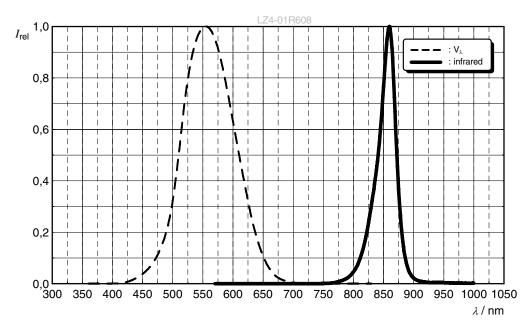
Example: PQ-F08

Brightness	Wavelength
PQ	F08



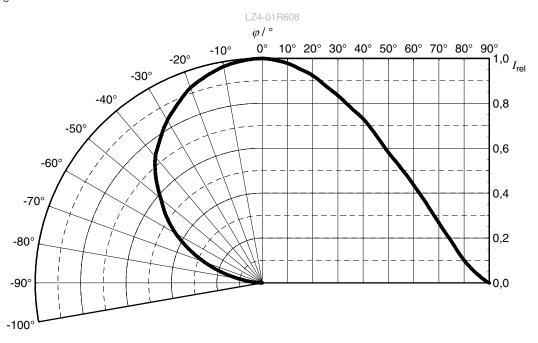
## Relative Spectral Emission 6)

 $I_{rel}$  = f ( $\lambda$ );  $I_F$  = 700 mA;  $T_C$  = 25 °C



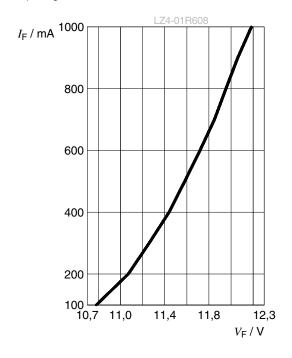
#### Radiation Characteristics 6)

 $I_{rel} = f (\phi); T_C = 25 °C$ 



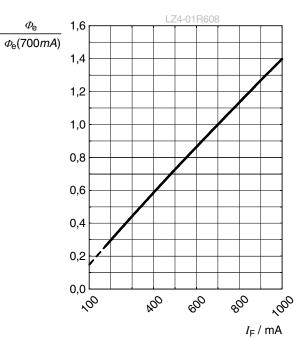
#### Forward current 6)

$$I_F = f(V_F); T_C = 25 \, ^{\circ}C$$



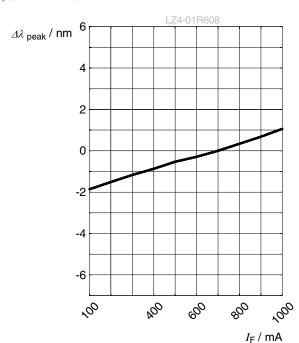
#### Relative Radiant Power 6), 7)

$$\Phi_{\rm E}/\Phi_{\rm E}(700 {\rm mA}) = f(I_{\rm F}); T_{\rm C} = 25 {\rm °C}$$



# Peak Wavelength 6)

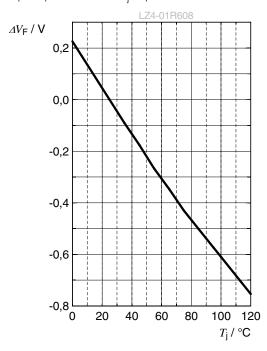
$$\Delta \lambda_{\text{peak}} = f(I_F); T_C = 25 \text{ }^{\circ}\text{C}$$





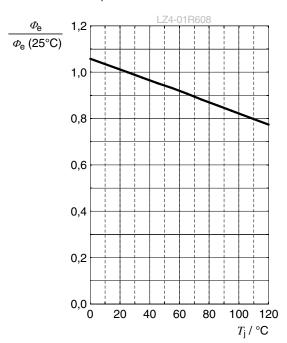
## Forward Voltage 6)

$$\Delta V_{_F} = V_{_F} - V_{_F} (25 \ ^{\circ}C) = f(T_{_j}); \ I_{_F} = 700 \ \text{mA}$$



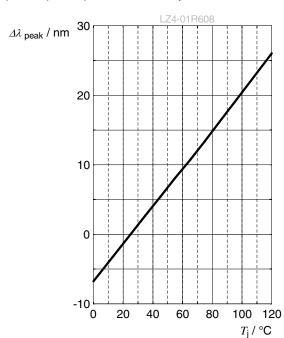
#### Relative Radiant Power 6)

$$\Phi_{\rm E}/\Phi_{\rm E}(25~{\rm ^{\circ}C}) = f(T_{\rm i}); I_{\rm F} = 700~{\rm mA}$$



# Peak Wavelength 6)

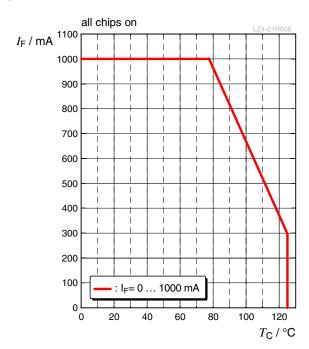
$$\Delta \lambda_{\text{peak}} = \lambda_{\text{peak}} - \lambda_{\text{peak}} (25 \text{ °C}) = \text{f(T}_{\text{j}}); \text{ I}_{\text{F}} = 700 \text{ mA}$$





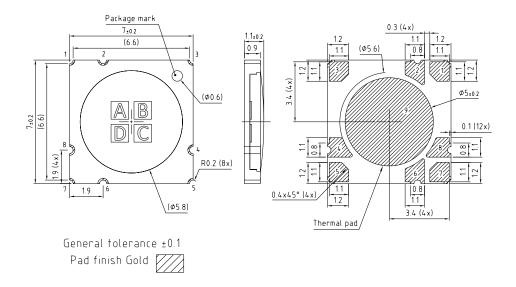
## Max. Permissible Forward Current 8)

 $I_F = f(T)$ 





# Dimensional Drawing 9)



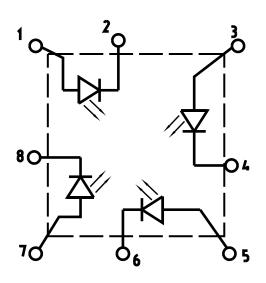
C67062-A0375-A3..-02

### **Further Information:**

**Approximate Weight:** 163.0 mg

## **Electrical Internal Circuit**

	Pin Out			
Pad	Die	Function		
1	Α	Anode		
2	A	Cathode		
3	В	Anode		
4	В	Cathode		
5	C	Anode		
6	C	Cathode		
7	D	Anode		
8	D	Cathode		
9	n/a	Thermal		

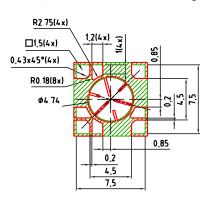


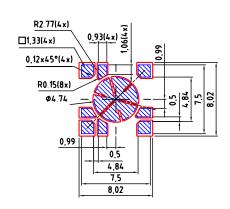


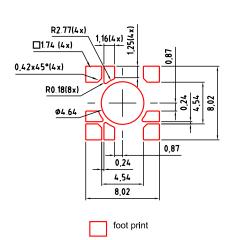
#### Recommended Solder Pad 9)

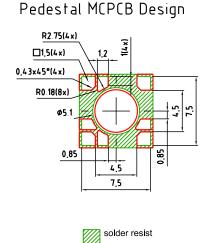
#### 1,16(4x) 1,25(4x) R2.77(4x) □1.74 (4 x) 0.42×45°(4×) R0.18(8x 0.24 Ø0 75 ø5 Ø2.5 0.87 0,24 4.54 8 02

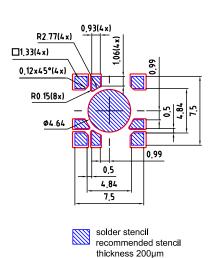
#### Non-pedestal MCPCB Design



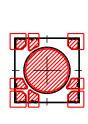


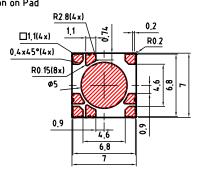






Component Location on Pad





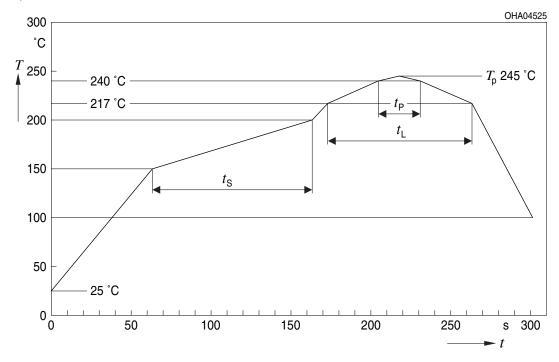
E062.3010.291-01

- 1. For superior solder joint connectivity results we recommend soldering under standard nitrogen atmosphere.
- 2. Package not suitable for ultra sonic cleaning.
- 3. Pedestal MCPCB allows the emitter thermal slug to be soldered directly to the metal core of the MCPCB. Such MCPCB eliminate the high thermal resistance dielectric layer that standard MCPCB technologies use in between the emitter thermal slug and the metal core of the MCPCB, thus lowering the overall system thermal resistance.
- 4. X-ray sample monitoring for solder voids underneath the emitter thermal slug is recommended. The total area covered by solder voids should be less than 20% of the total emitter thermal slug area. Excessive solder voids will increase the emitter to MCPCB thermal resistance and may lead to higher failure rates due to thermal over stress.



## **Reflow Soldering Profile**

Product complies to MSL Level 1 acc. to JEDEC J-STD-020E



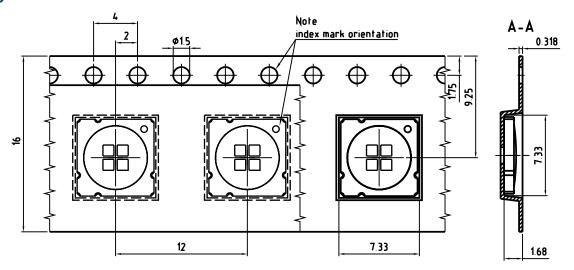
Profile Feature	Symbol	Pb-Free (SnAgCu) Assembly		Unit	
		Minimum	Recommendation	Maximum	
Ramp-up rate to preheat*)	'		2	3	K/s
25 °C to 150 °C					
Time t <sub>s</sub>	t <sub>s</sub>	60	100	120	S
$T_{Smin}$ to $T_{Smax}$					
Ramp-up rate to peak*)			2	3	K/s
$T_{Smax}$ to $T_{P}$					
Liquidus temperature	$T_{L}$		217		°C
Time above liquidus temperature	$t_{\scriptscriptstyle \perp}$		80	100	S
Peak temperature	T <sub>P</sub>		245	260	°C
Time within 5 °C of the specified peak temperature T <sub>p</sub> - 5 K	t <sub>P</sub>	10	20	30	S
Ramp-down rate* T <sub>P</sub> to 100 °C			3	6	K/s
Time 25 °C to T <sub>P</sub>				480	S

All temperatures refer to the center of the package, measured on the top of the component

<sup>\*</sup> slope calculation DT/Dt: Dt max. 5 s; fulfillment for the whole T-range



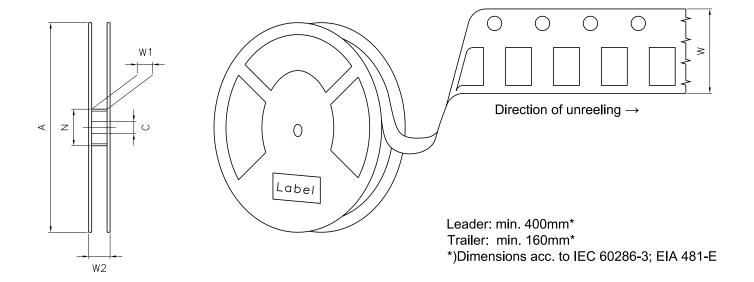
# Taping 9)



C67062-A0375-B1-01



## Tape and Reel 10)



### **Reel Dimensions**

Α	W	$N_{\min}$	$W_1$	$W_{2\text{max}}$	Pieces per PU
180 mm	16 + 0.3 / - 0.1 mm	60/100 mm	16.4 + 2 mm	22.4 mm	500



# Barcode-Product-Label (BPL)

**Opto Semiconductors** 

Our Brand

LED ENGIN

(6P) Batch No: 1234567890 (1T) Lot No: 1234567890

(X) Prod No: 12345678

(9D) D/C: 1234

(Q) Qty: 9999

CoO: XX

BIN1: XXX-X-X-XXX LXX-XXXXX BIN2: XXX-X-X-XXX

XxxXxx BIN3: XX-X-X-XXX

BIN4: XXX-XXX-X-XXX RoHS Compliant

BIN5: X-XX-X-XXX BIN6: X-XX-X-XXX

TEMP ST

Pack: RXX

B X123 12345.1234







#### **Notes**

The evaluation of eye safety occurs according to the standard IEC 62471:2006 (photo biological safety of lamps and lamp systems). Within the risk grouping system of this IEC standard, the device specified in this data sheet fall into the class moderate risk (exposure time 0.25 s). Under real circumstances (for exposure time, conditions of the eye pupils, observation distance), it is assumed that no endangerment to the eye exists from these devices. As a matter of principle, however, it should be mentioned that intense light sources have a high secondary exposure potential due to their blinding effect. When looking at bright light sources (e.g. headlights), temporary reduction in visual acuity and afterimages can occur, leading to irritation, annoyance, visual impairment, and even accidents, depending on the situation.

Subcomponents of this device contain, in addition to other substances, metal filled materials including silver. Metal filled materials can be affected by environments that contain traces of aggressive substances. Therefore, we recommend that customers minimize device exposure to aggressive substances during storage, production, and use. Devices that showed visible discoloration when tested using the described tests above did show no performance deviations within failure limits during the stated test duration. Respective failure limits are described in the IEC60810.

Tapes and reels are shipped in airtight bags in order to reduce the onset of silver tarnish. We recommend bags only be opened when ready to use emitters. Partially used reels or trays should be stored in airtight bags or in storage purged with nitrogen.

Based on very short life cycle times in chip technology this component is subject to frequent adaption to the latest chip technology.

Changes to the content of this datasheet may occur without further notification. JEDEC 46C constitutes the guideline of the change management for the device specified in this document.

For further application related information please visit https://ams-osram.com/support/application-notes



#### Disclaimer

#### Attention please!

The information describes the type of component and shall not be considered as assured characteristics. Terms of delivery and rights to change design reserved. Due to technical requirements components may contain dangerous substances.

For information on the types in question please contact our Sales Organization.

If printed or downloaded, please find the latest version on our website.

#### **Packing**

Please use the recycling operators known to you. We can also help you – get in touch with your nearest sales office. By agreement we will take packing material back, if it is sorted. You must bear the costs of transport. For packing material that is returned to us unsorted or which we are not obliged to accept, we shall have to invoice you for any costs incurred.

#### Product and functional safety devices/applications or medical devices/applications

Our components are not developed, constructed or tested for the application as safety relevant component or for the application in medical devices.

Our products are not qualified at module and system level for such application.

In case buyer – or customer supplied by buyer – considers using our components in product safety devices/ applications or medical devices/applications, buyer and/or customer has to inform our local sales partner immediately and we and buyer and /or customer will analyze and coordinate the customer-specific request between us and buyer and/or customer.



#### Glossary

- Brightness: Brightness groups are tested at a current pulse duration of 10 ms and a tolerance of ±10 %.
- 2) Operating Conditions: Operating conditions according DC-derating (Max. Permissible Forward Current)
- 3) Reverse Operation: Not designed for reverse operation. Continuous reverse operation can cause migration and damage of the device.
- Peak Wavelength: Wavelengths are tested at a current pulse duration of 10 ms and a tolerance of ±2 nm.
- 5) Forward Voltage: Forward voltages are tested at a current pulse duration of 10 ms and a tolerance of ±0.1 V.
- 6) Typical Values: Due to the special conditions of the manufacturing processes of semiconductor devices, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.
- Characteristic curve: In the range where the line of the graph is broken, you must expect higher differences between single devices within one packing unit.
- 8) Thermal Resistance: Rth max is based on statistic values (6 $\sigma$ ) used for Derating.
- 9) Tolerance of Measure: Unless otherwise noted in drawing, tolerances are specified with ±0.1 and dimensions are specified in mm.
- Tape and Reel: All dimensions and tolerances are specified acc. IEC 60286-3 and specified in mm.



# **Revision History**

	•	
Version	Date	Change
1.2	2021-07-14	New Layout
1.3	2022-05-17	New Layout
1.4	2023-12-05	Applications Dimensional Drawing
1.5	2025-06-05	Product Image



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